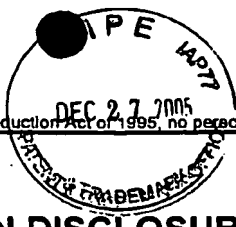


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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/533,141
Filing Date	April 27, 2005
First Named Inventor	Jun-ichi Okamura
Art Unit	Confirmation No. 5031 — 2816
Examiner Name	DIANA CHENG
Attorney Docket Number	1032-0002WOUS

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/DC/		James H. Atherton and H. Thomas Simmonds, "An Offset Reduction Technique for Use with CMOS Integrated Comparators and Amplifiers", 8107 IEEE, 08/27/92; New York, US	

Examiner Signature	/Diana Cheng/	Date Considered	06/09/2007
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JC20 Rec'd PCT 27 APR 2005

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Approved for use through 07/31/2008. OMB 0851-0031

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Application Number

Filing Date

First Named Inventor

Art Unit

Examiner Name

Attorney Docket Number

Sheet 1

of 1

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ^o
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			
/DC/		JP 7-58872 B2	06-21-95	Toshiba		✓
/DC/		JP 7-193439 A	07-28-95	Fujitsu		✓

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Signature**

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Date Considered

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